

FIG. 1

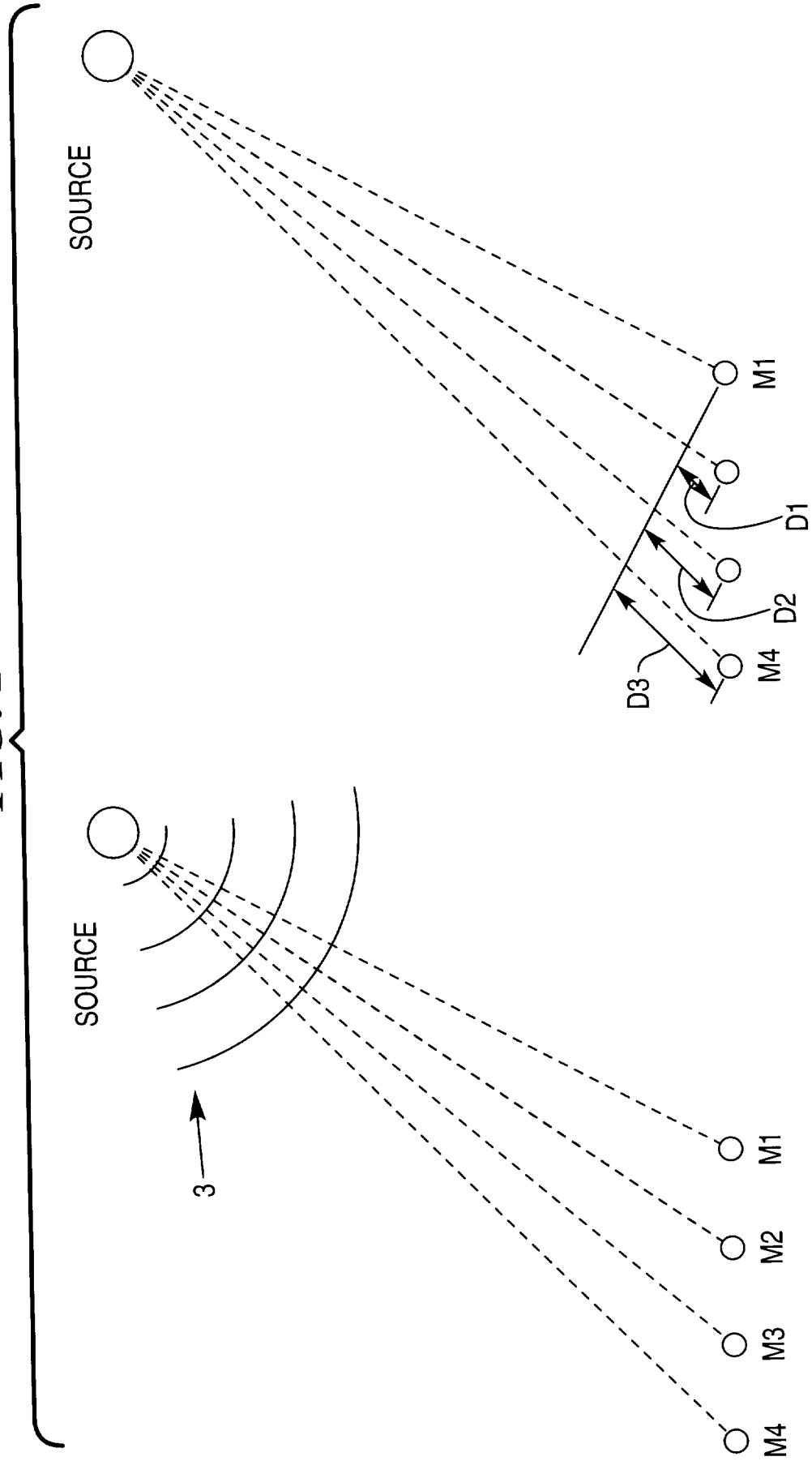


FIG. 2

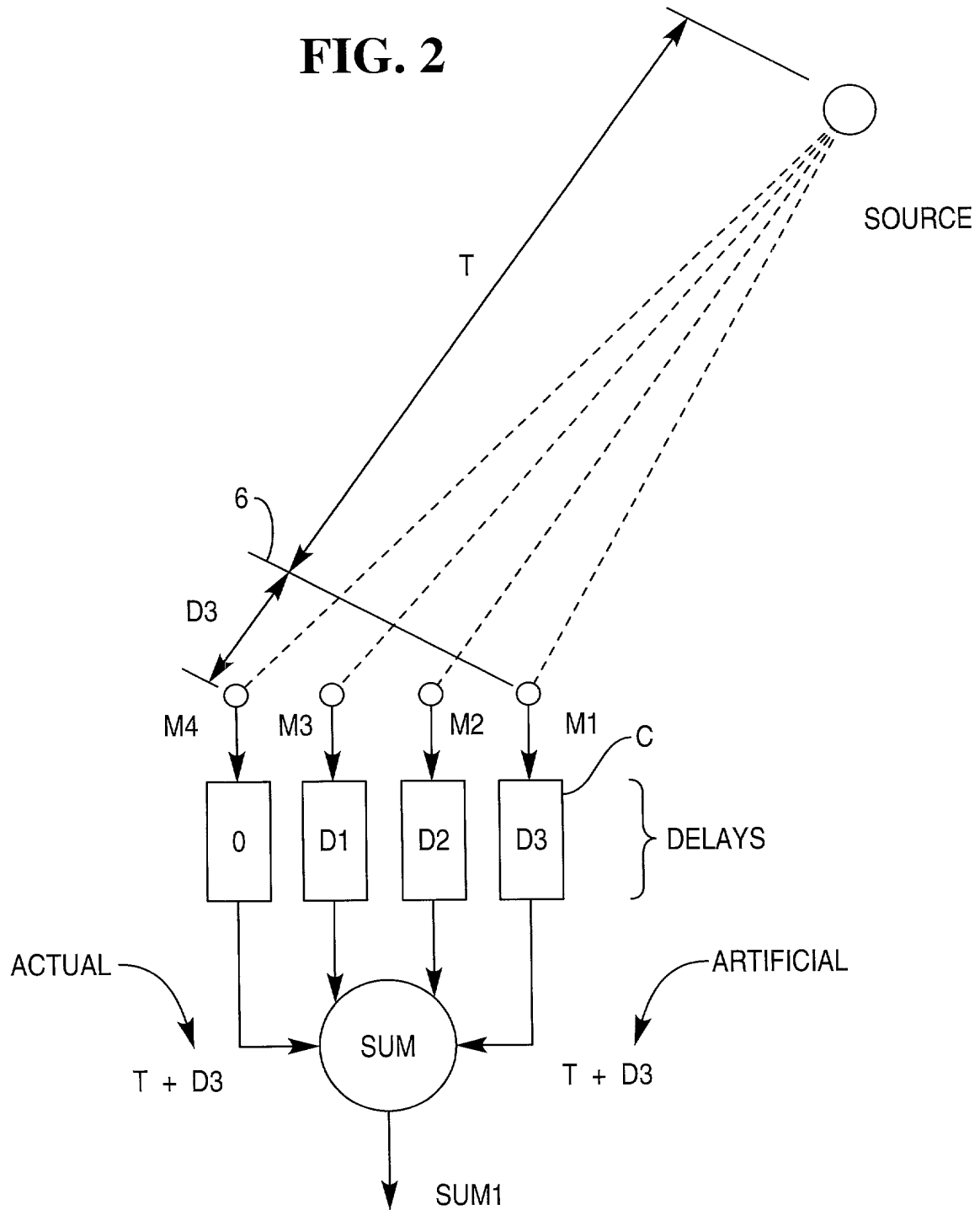


FIG. 3

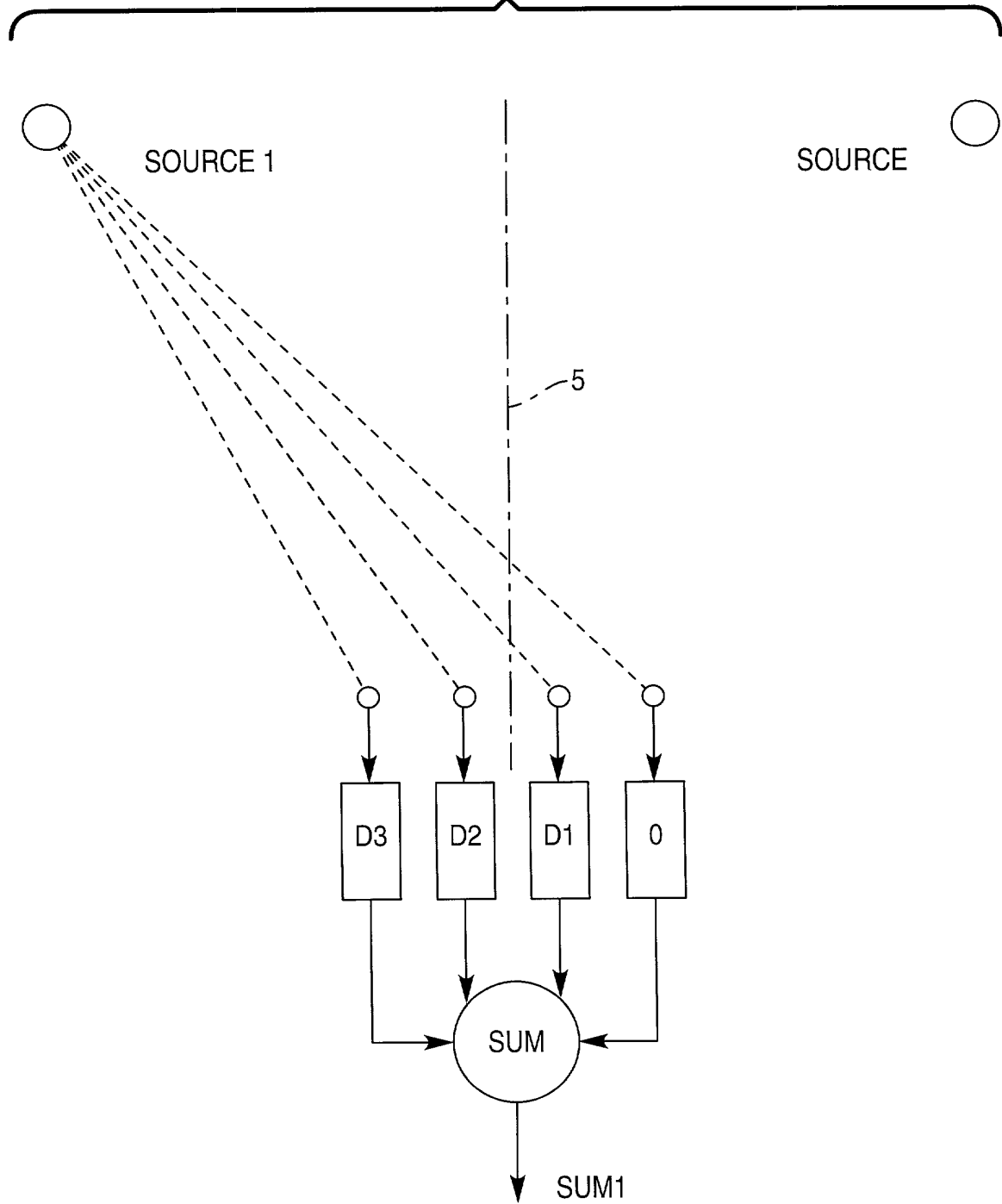
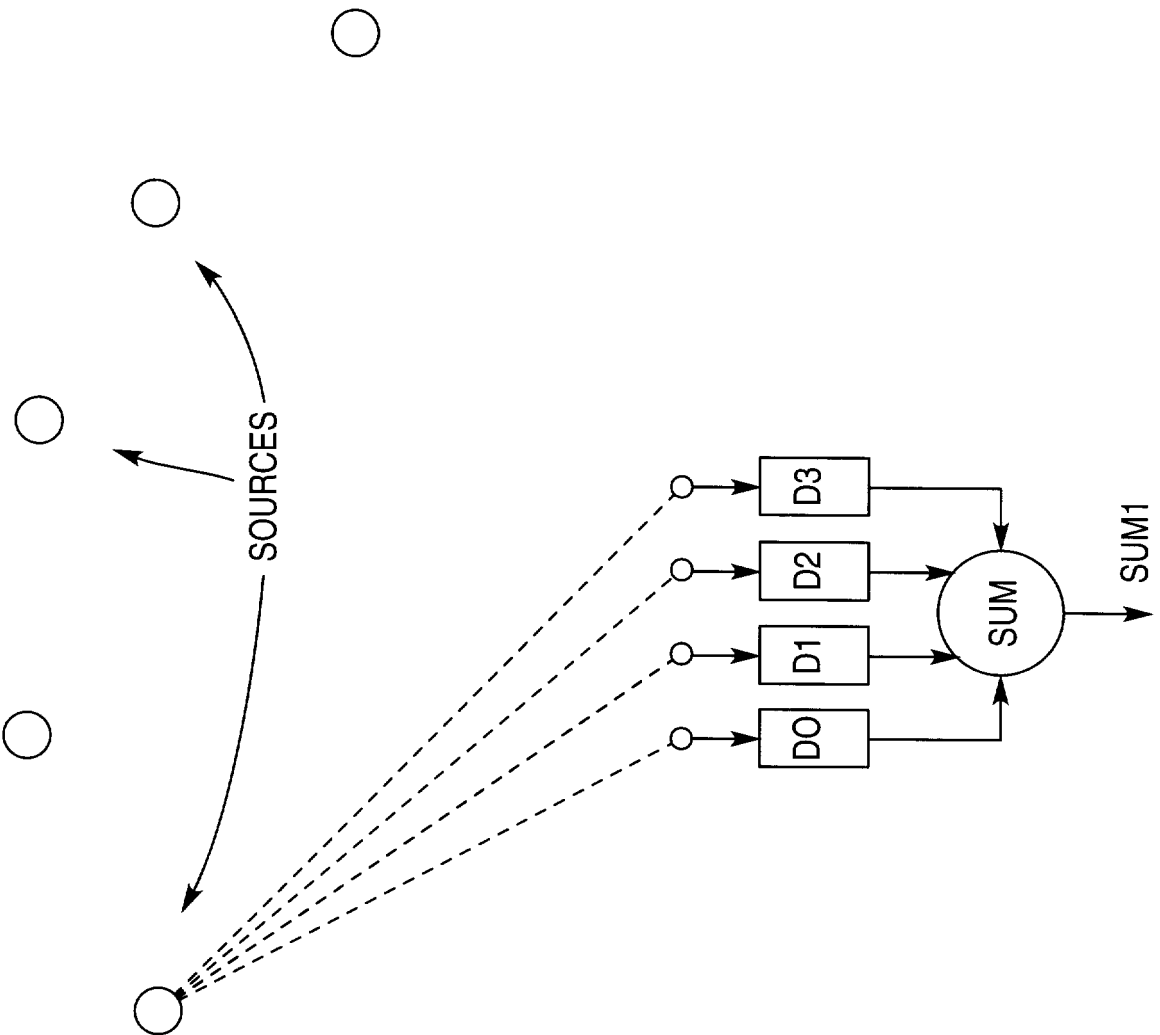


FIG. 4 is a schematic diagram of a system for summing signals from multiple sources. The system includes a set of sources, a delay set, and a summing circuit. The sources are connected to the delay set, which is connected to the summing circuit. The summing circuit outputs a sum signal.



LOBE	DELAY SET
L1	1
L2	2
L3	3
L4	4
	etc.

TABLE

FIG. 4

FIG. 5

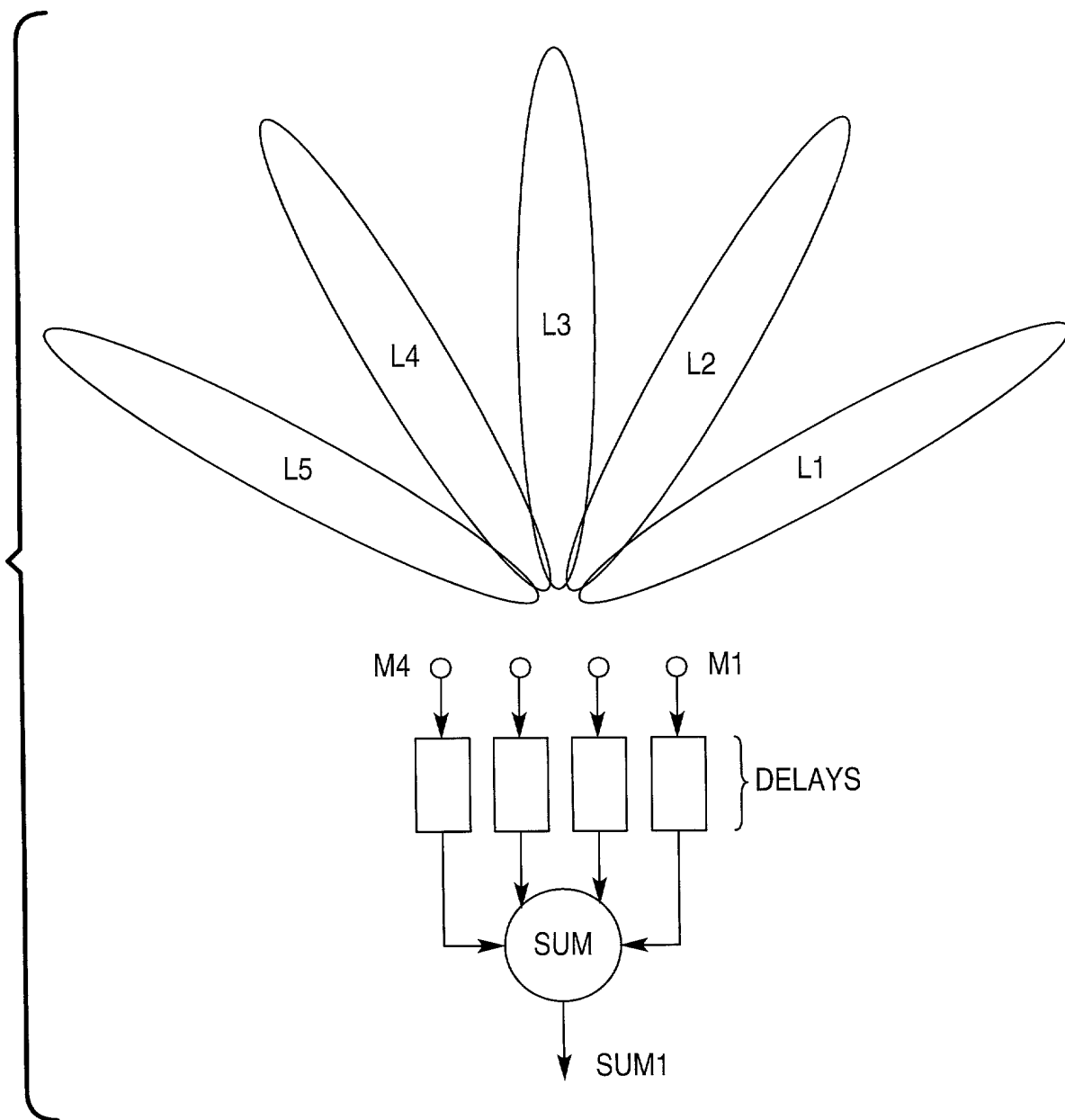


FIG. 6

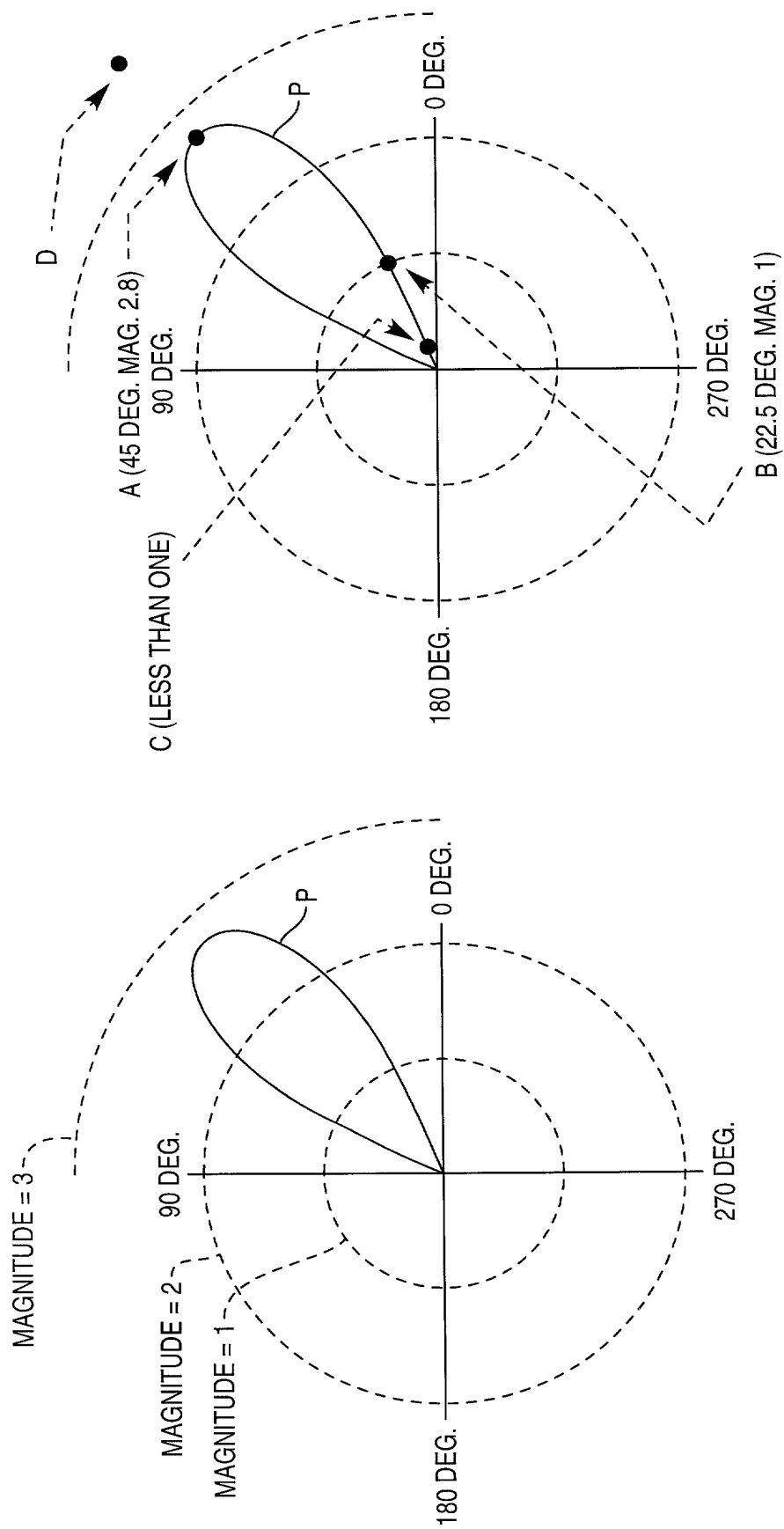


FIG. 7

